Docket No.: 42390P7719

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re the Application of:

RAJESH R. SHAH, ET AL.

Application No.: 09/450,381

Filed: November 29, 1999

For: A Cluster With Multiple Paths Between

**Hosts And I/O Controllers** 

Art Group: 2126

Examiner: Anya, Charles E.

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## INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed for applications filed after June 30, 2003). This IDS and IDS Citation Form are being submitted within three (3) months of filing of the application. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF TAYOOR & ZAFMAN LLP

Date: September 14, 2004

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> I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Annie G. Pearson

Date

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Technology Center 2000 Substitute for form 1449A/PTO Complete if Known Application Number 09/450,381 **INFORMATION DISCLOSURE** Filing Date November 29, 1999 First Named Inventor Rajesh R. Shah STATEMENT BY APPLICANT Art Unit 2126 **Examiner Name** Anya, Charles E. Attorney Docket Number Sheet of 1 4239<u>0</u>P7719

NON PATENT LITERATURE DOCUMENTS						
Examiner Initials*	Cite No.¹					
	1	Next Generation I/O Architecture Specification: Channel Interface Specification, Final Draft: March 26, 1999.				
	2	Next Generation I/O Link Architecture Specification: Fabric Service Interface Specification, Final Draft: March 26, 1999.				
	3	Next Generation I/O Link Architecture Specification: HCA Specification, Final Draft: March 26, 1999.				
	4	Next Generation I/O Link Architecture Specification: Link Specification, Final Draft: March 26, 1999.				
****	5	Next Generation I/O: Principles of Operation, Final Draft: March 26, 1999.				
	6	Next Generation I/O Architecture: Switch Specification, Final Draft: March 26, 1999.				
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Examiner	Date	
Signature	Considered	

Based on PTO/SB/08B (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wtr) 08/11/2003. Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

<sup>\*</sup>Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include

<sup>&#</sup>x27;Applicant's unique citation designation number. 'Applicant is to place a check mark here if English language Translation is attached.